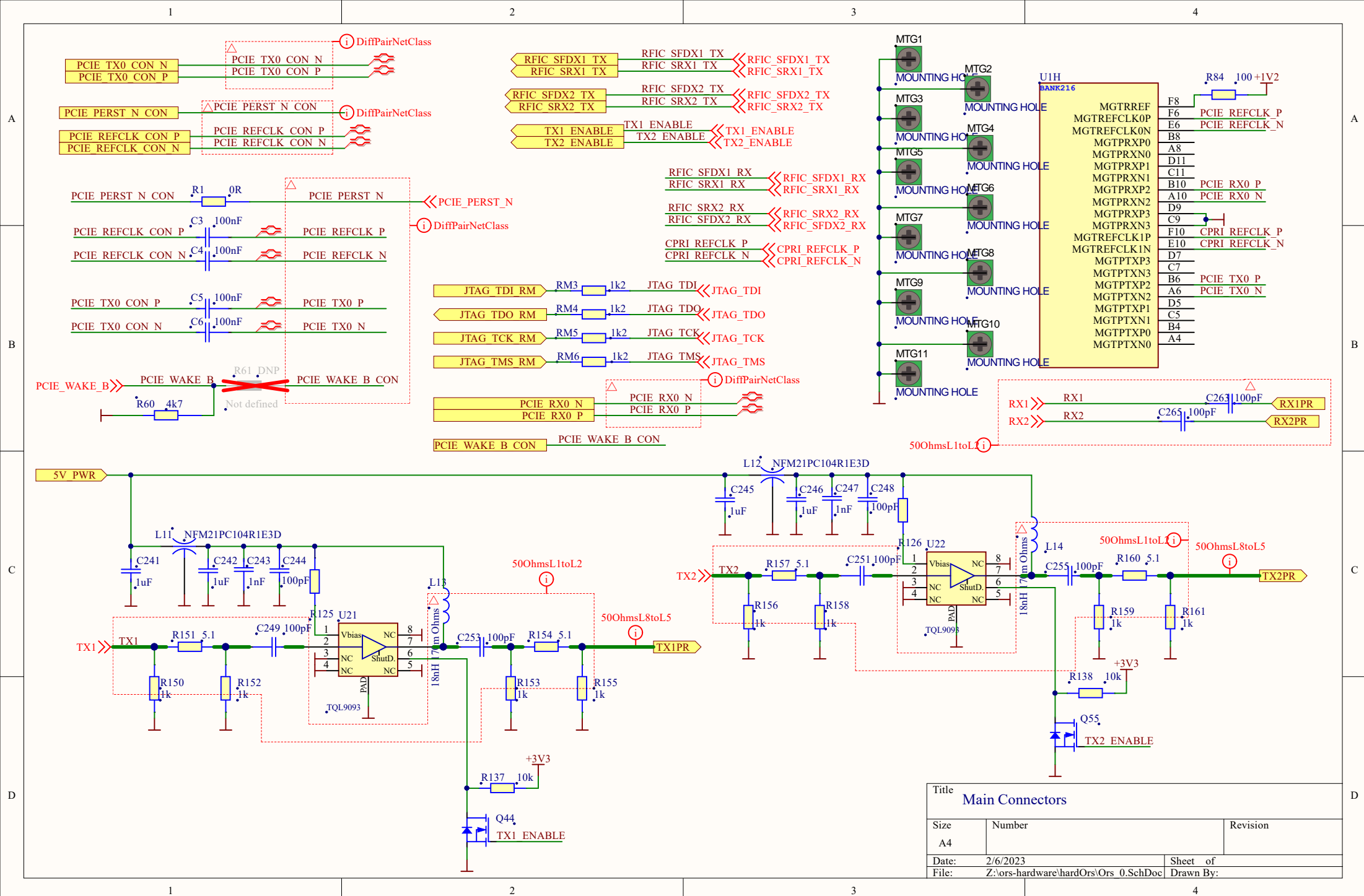
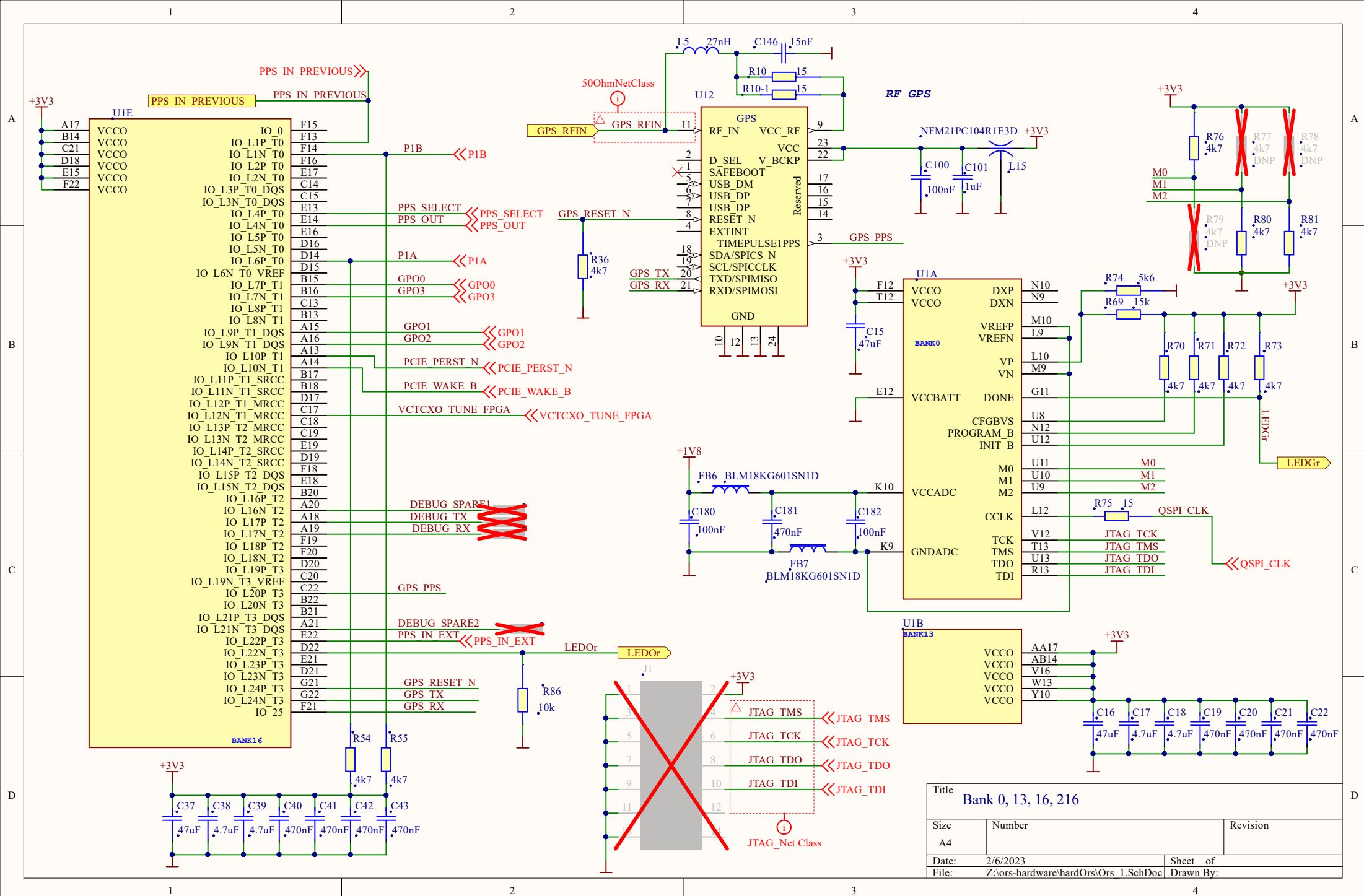


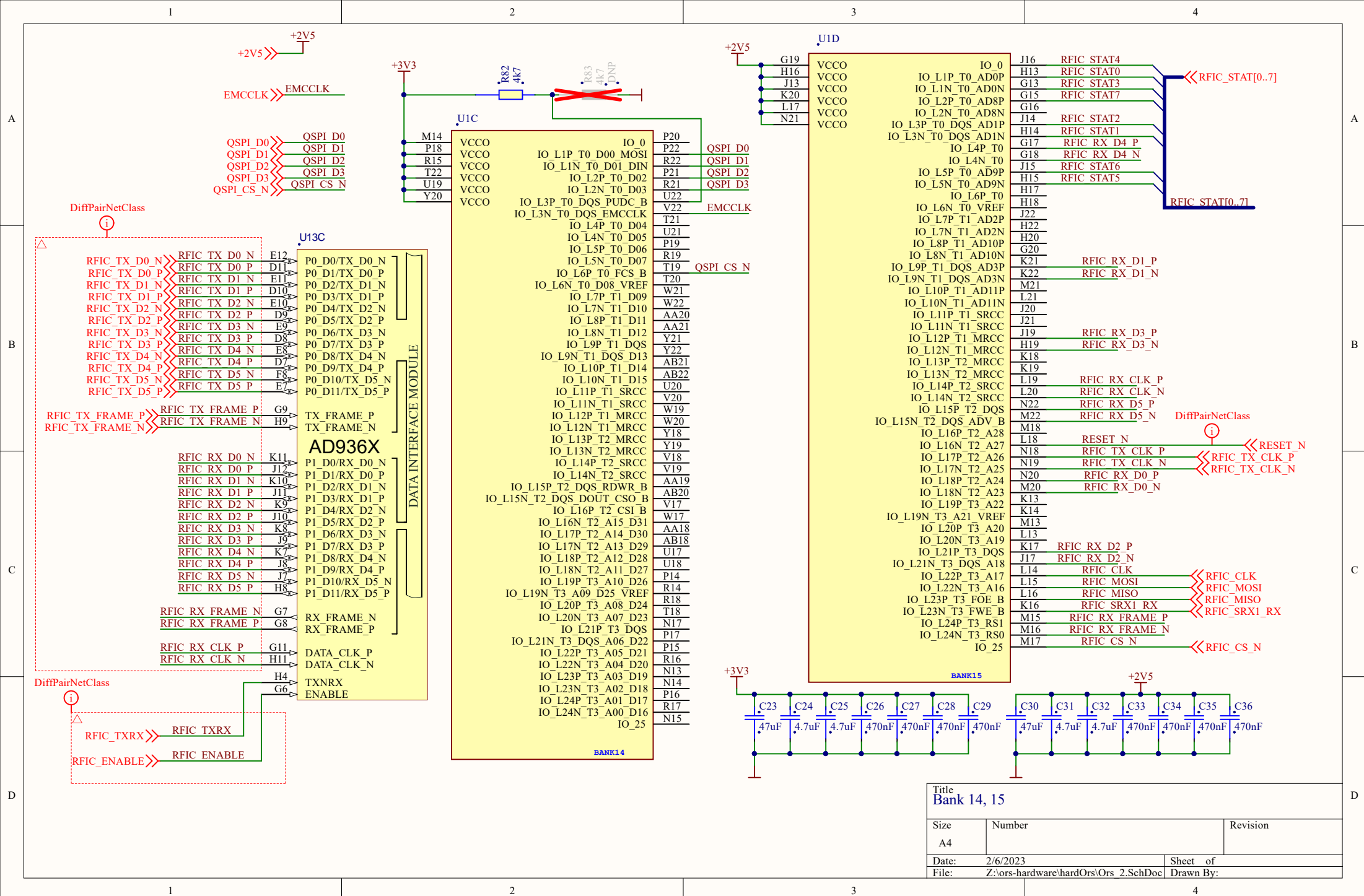
Title		
Size	Number	Revision
A4		
Date:	2/6/2023	Sheet of
File:	Z:\ors-hardware\...\Ors_Pwr_1.SchDoc	Drawn By:



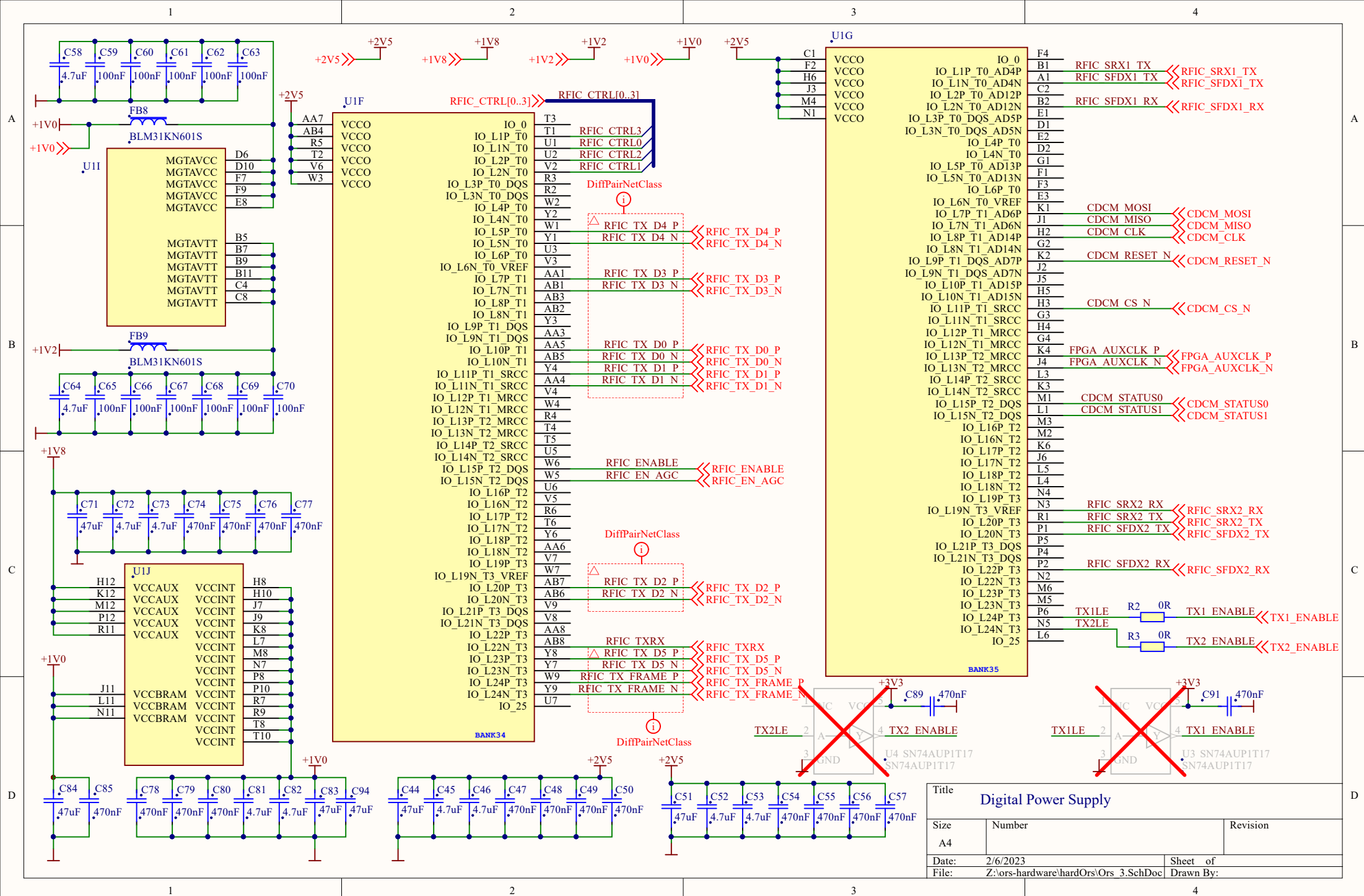










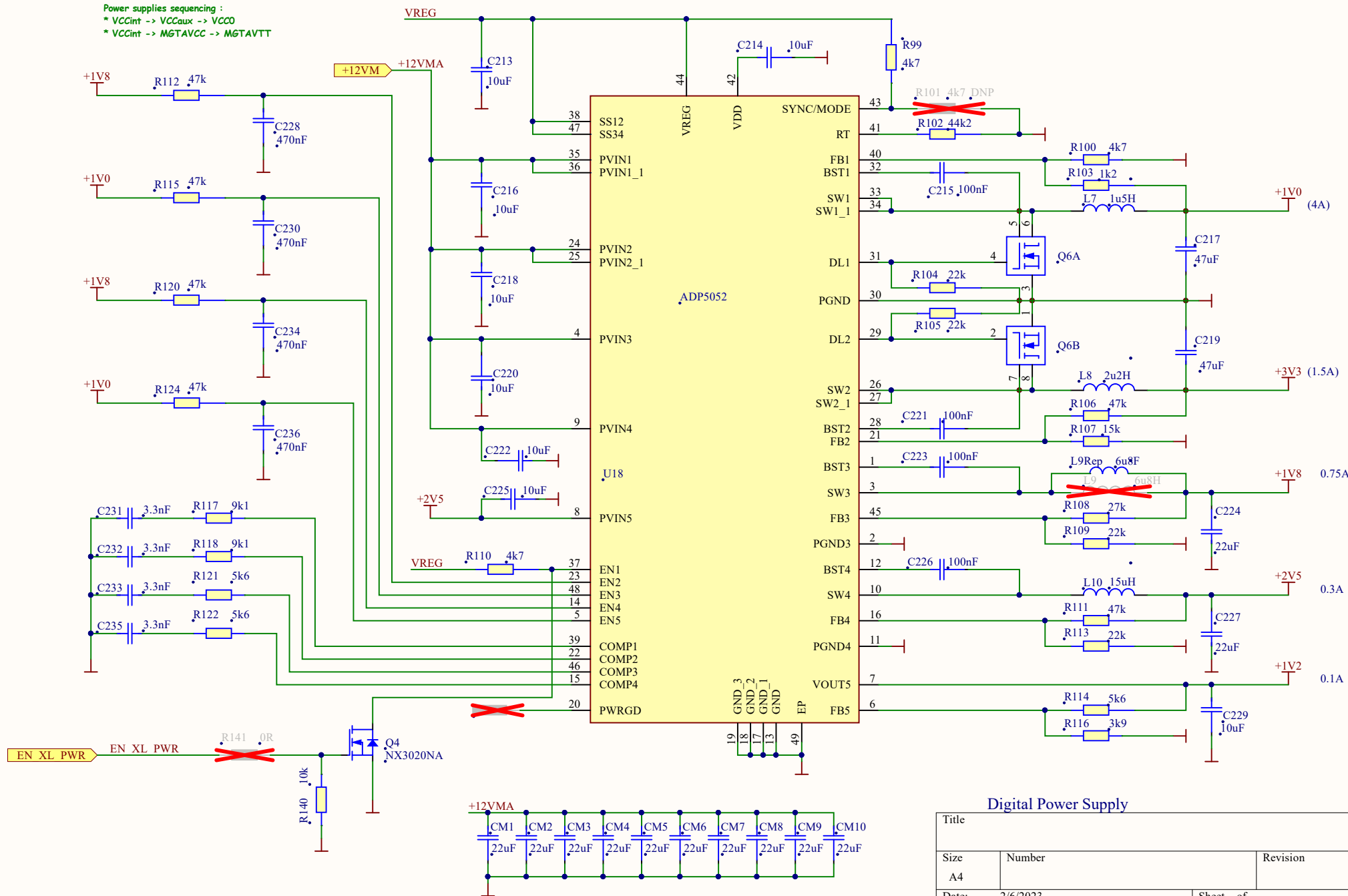






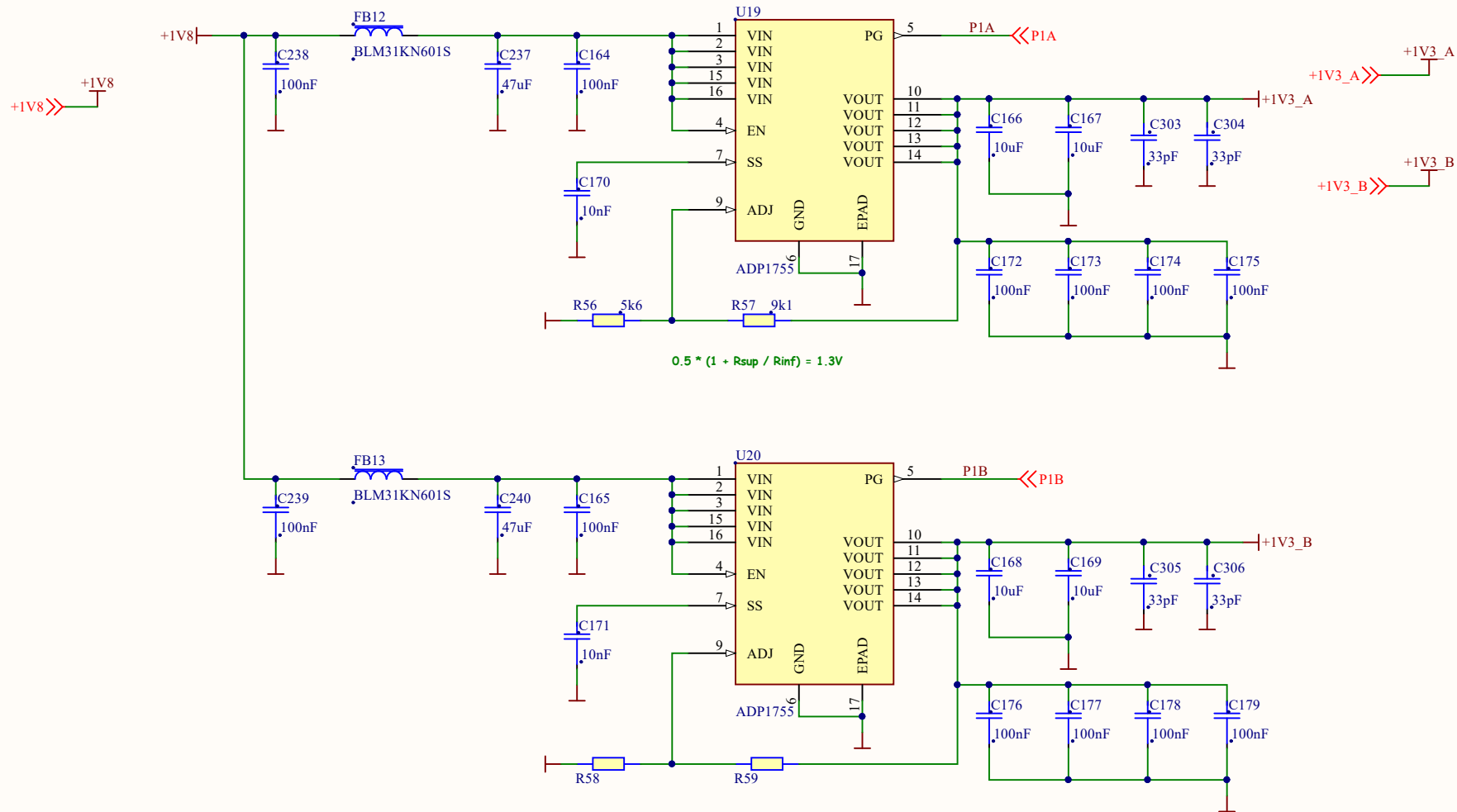


Power supplies sequencing :  
\* VCCint -> VCCaux -> VCCO  
\* VCCint -> MGTAVCC -> MGTAVTT



### Digital Power Supply

Title		
Size	Number	Revision
A4		
Date:	2/6/2023	Sheet of
File:	Z:\ors-hardware\hardOrs\Ors_6.SchDoc	Drawn By:



Title RF Power Supply		
Size A4	Number	Revision
Date: 2/6/2023	Sheet of	
File: Z:\ors-hardware\hardOrs\Ors_7.SchDoc	Drawn By:	

A

B

C

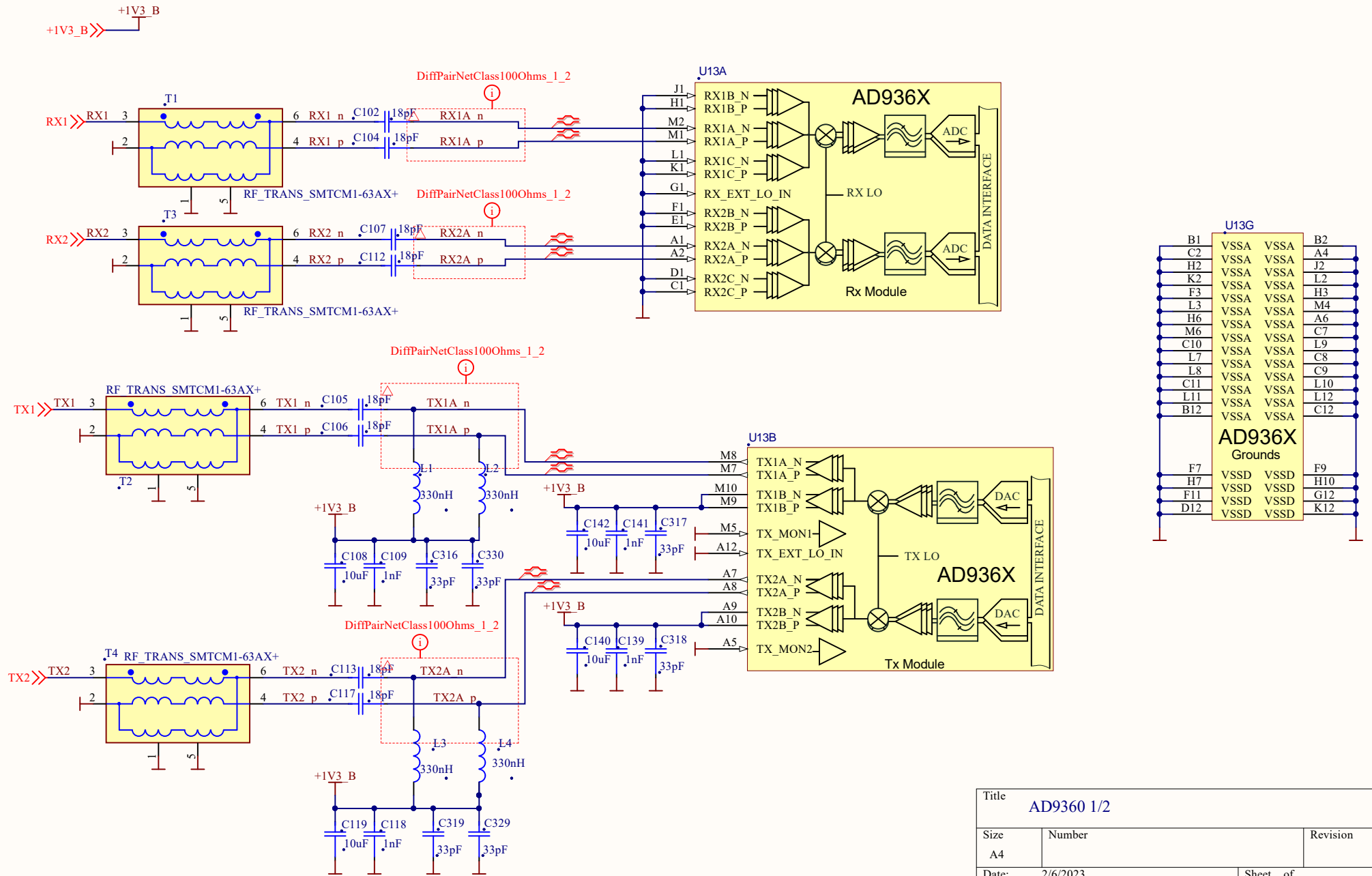
D

A

B

C

D







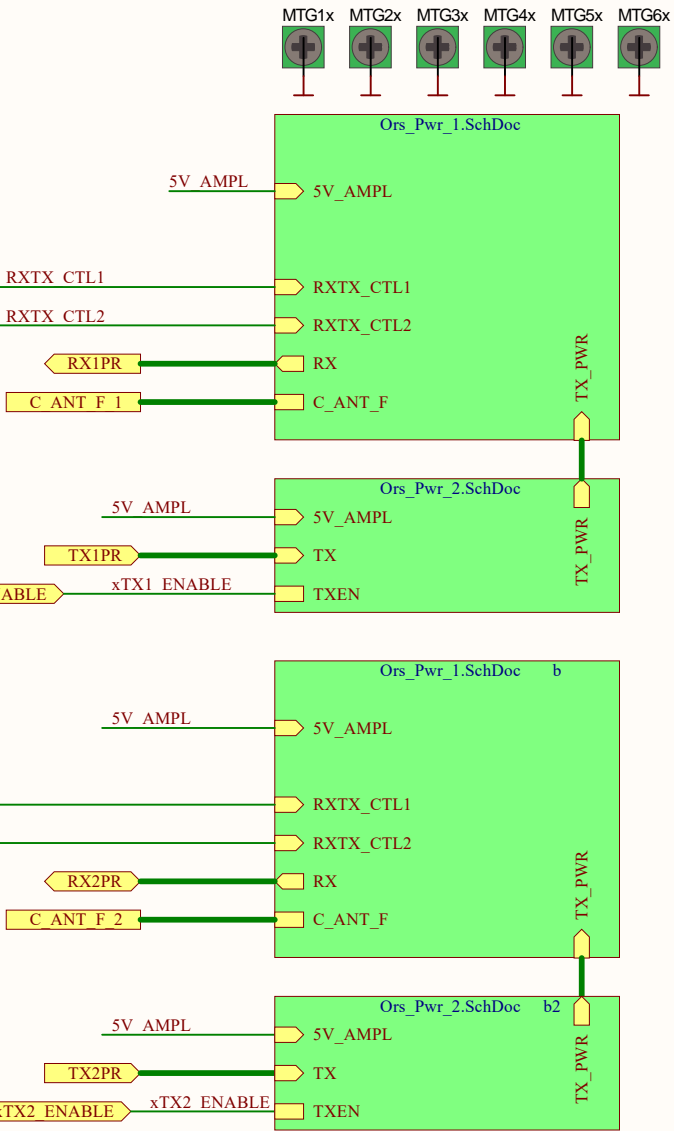
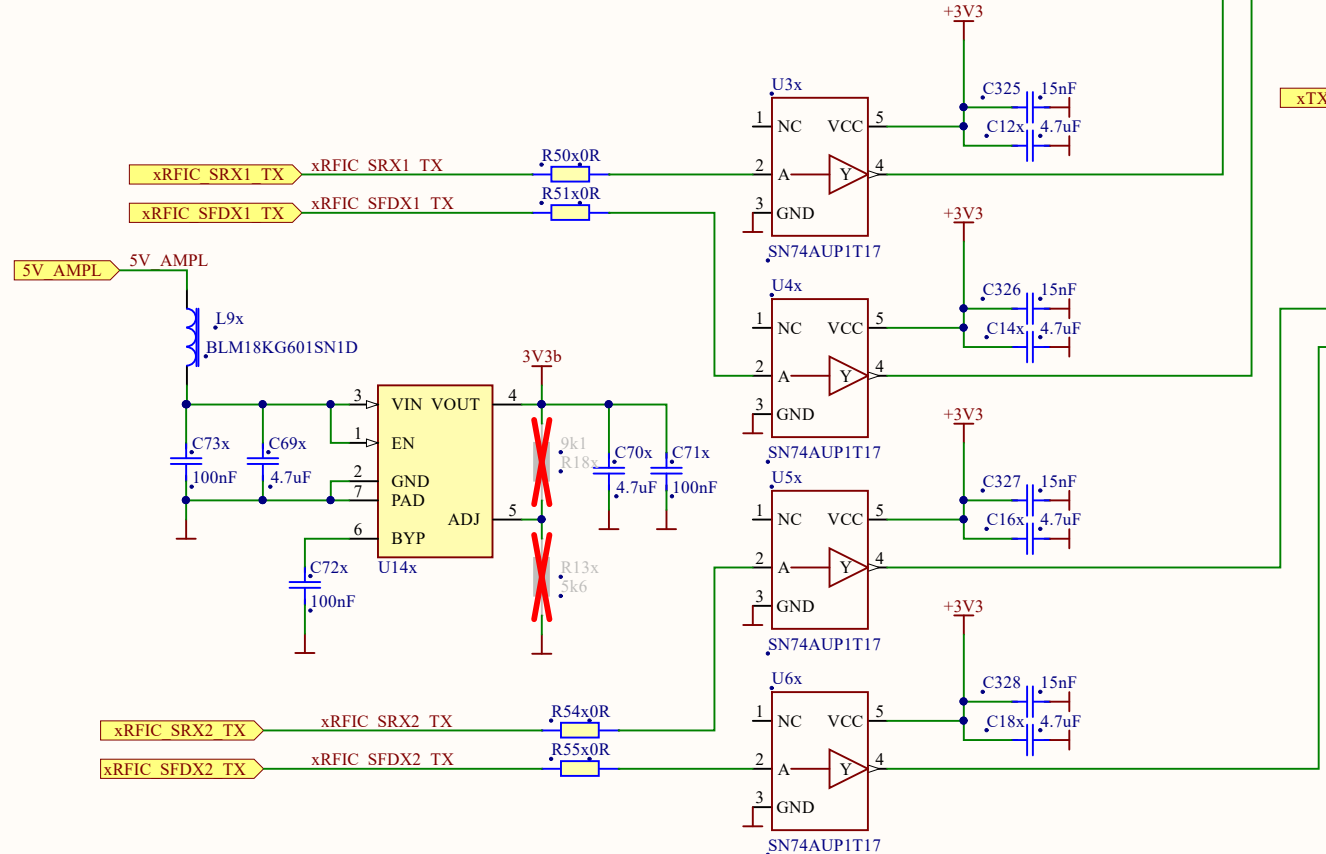
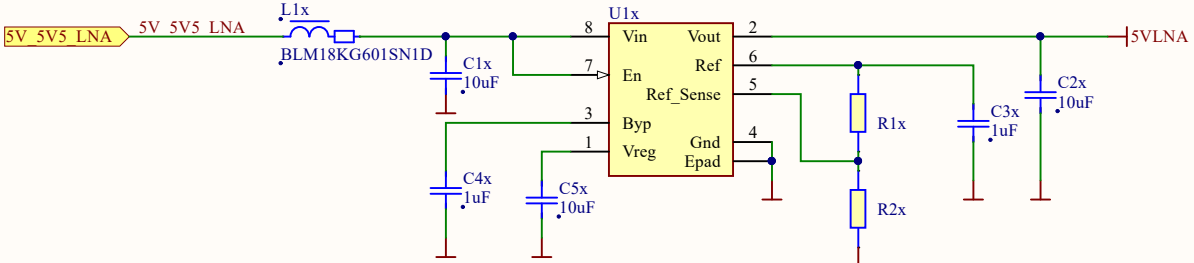


A

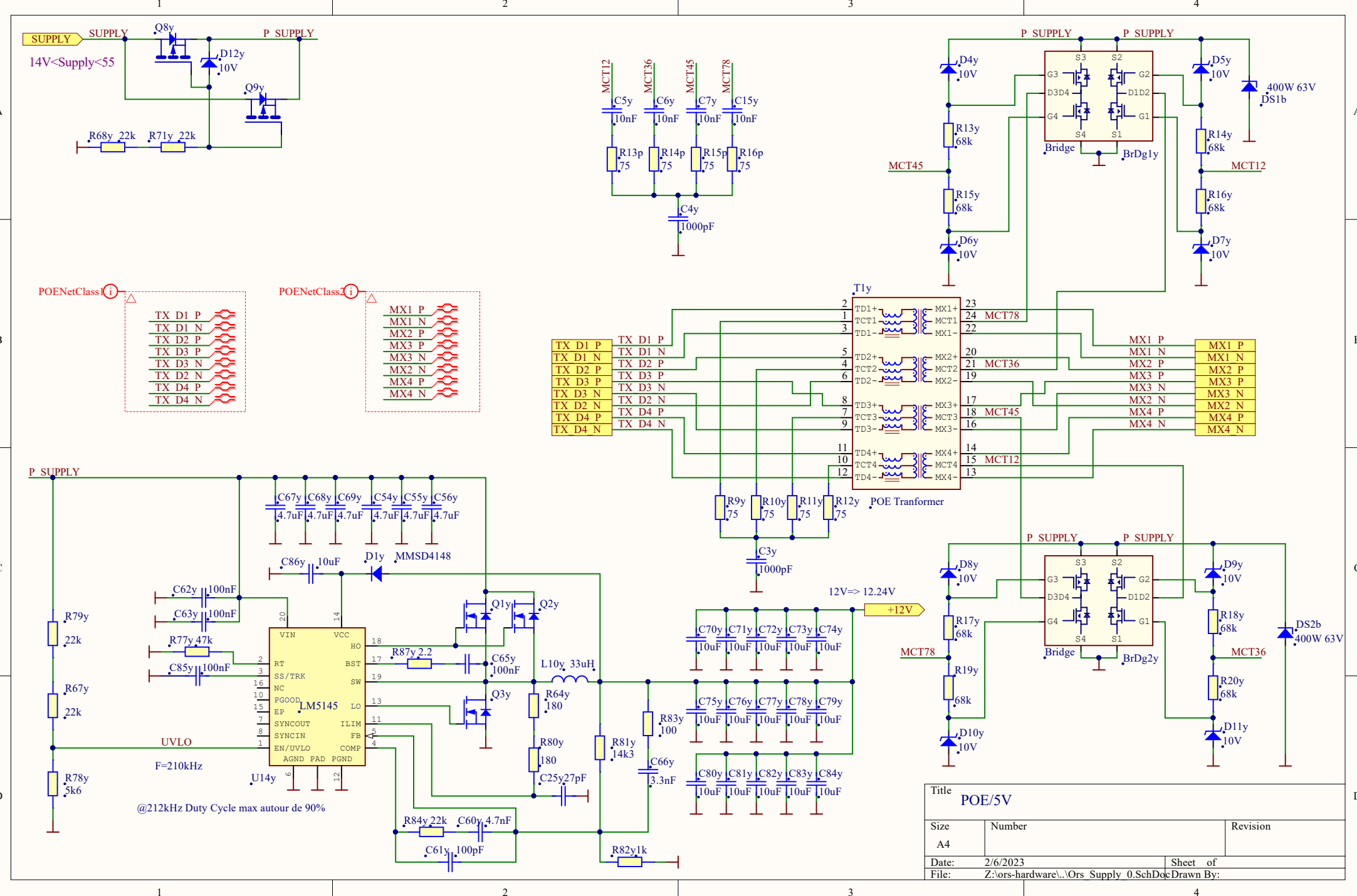
B

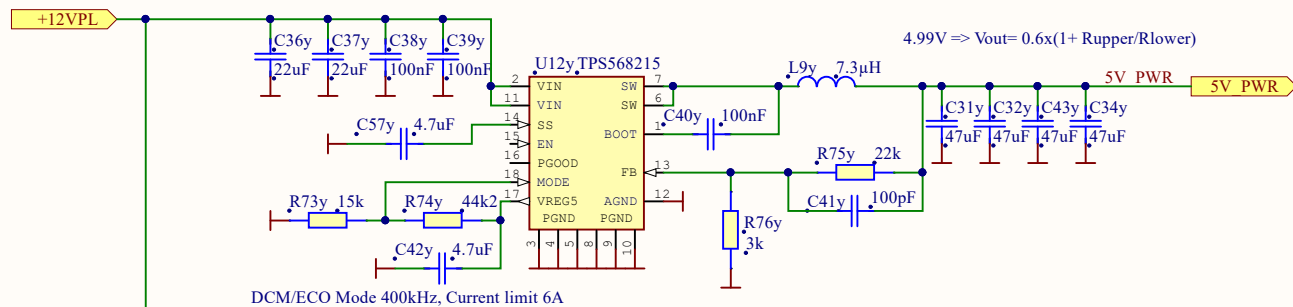
C

D

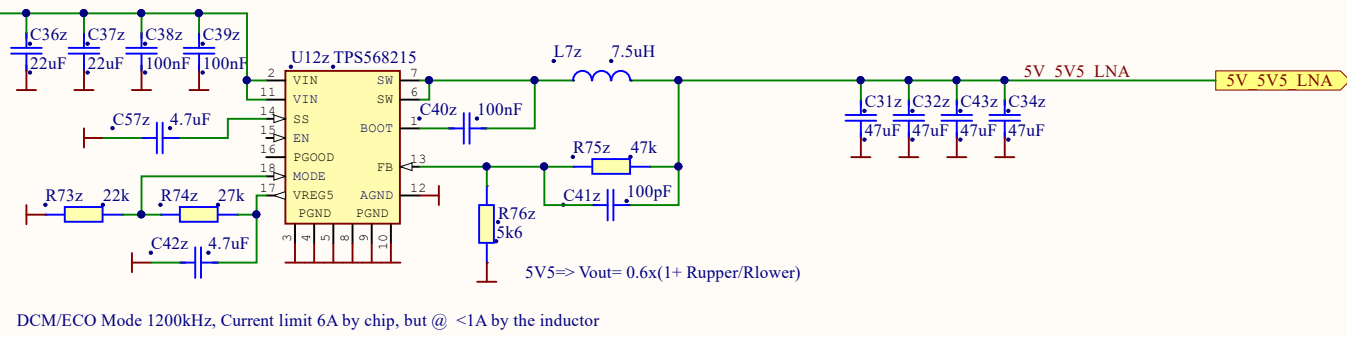


Title		
Size	Number	Revision
A4		
Date:	2/6/2023	Sheet of
File:	Z:\ors-hardware\...\Ors_Pwr_0.SchDoc	Drawn By:





Soft Start  
 $TSS(seconde) = CSSx0.6/6e-6 \Rightarrow 0.47s$



Title Main Connectors		
Size A4	Number	Revision
Date: 2/6/2023	Sheet of	
File: Z:\ors-hardware\...\Ors Supply 1.SchDoc	Drawn By:	

